Panel Standardization

Hello all,

Travis, JD, and I had a meeting about trying to implement a panel point naming/assigning standard so we can mitigate any inconsistency between designers. Keep in mind this targeted to AHUs with PXCMs but please apply this when applicable. Here are some highlights that we touched on:

1. **Order of point assignments** 
   1. **Analog Outputs w/ override** (8X-ML) – Valves, Dampers, VFD speeds etc.
   2. **Analog Inputs w/ 4-20mA** (8X) – Any devices that only have 4-20mA inputs. Or any LPACI points.
   3. **Analog Inputs** (8U) – Devices that have 0-10vdc input. \*If the device has the option for 4-20mA or 0-10vdc, 0-10vdc is favored. (unless distance is an issue)
   4. **Digital Inputs** (8-16DI)
   5. **Digital Outputs** (6R-M)
2. **Assign Order**
   1. Once in profile view, select the window with all of the unassigned points. Right click 🡪 Address Order 🡪 By point type and then by name.
   2. Assign points based by type, then by alphabetical order
   3. Typically CCV will be your first point to assign.
   4. Group all like devices. Valves, Dampers, Temps, etc.
3. **Uniform**
   1. When assigning points keep in mind how these devices will be installed out in the field. The possibility of a multi-conductor picking up multiple points. VFDs speed, fault, feedback (\*note all ABB feedbacks are 4-20mA). Valve command, position feedback, end switches. In cases like this it is good practice to keep points assigned to the same location on their respective module. Or at a minimum for them to be placed on the same side of the module.

Example: Valve command is addressed to point 0.1.1, valve position feedback should be assigned to 0.2.1.

Of course this might not be possible in every situation but this should be a good guideline to follow. These are a few items that we discussed but if you have questions or have any other ideas to help implement some standardization, please feel free to share any thoughts or concerns. I will also be working with Travis to get an updated Field Panel Point Naming sheet and will share once that I complete.

Thanks,

JV Notes:

How should we add TXIO to PXCM? In general, what order should they be placed in?

How I did it at ACC Rio Grande:

First place AO 4-20mA (TXM1.8X-ML). These blocks may contain 4-20mA AO, and probably 4-20mA AI or other assorted AI.

8UML. These will usually be standard AO points for dampers, VFD etc. They will have paired points either directly on the 8UML or on the 8U modules below it

8U. These will be independent analog inputs, or they may be paired with AO on the 8UML above it.

All DO points – paired with DI below it

All DI points

Summary:

1. TXM8X-ML
2. TXM1.8U-ML
3. TXM1.8U
4. TXM1.6R-M
5. TXM1.16D